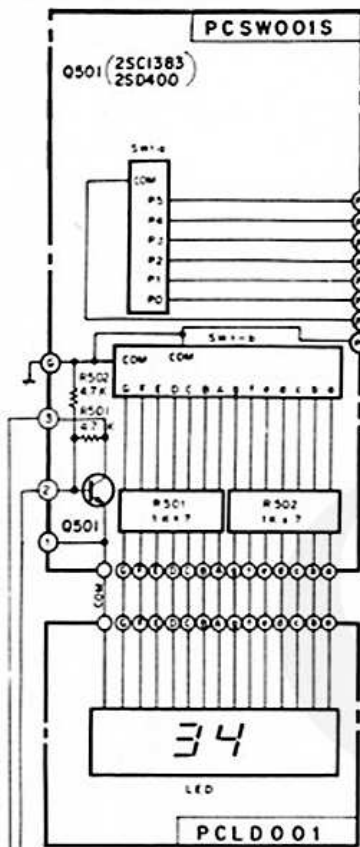


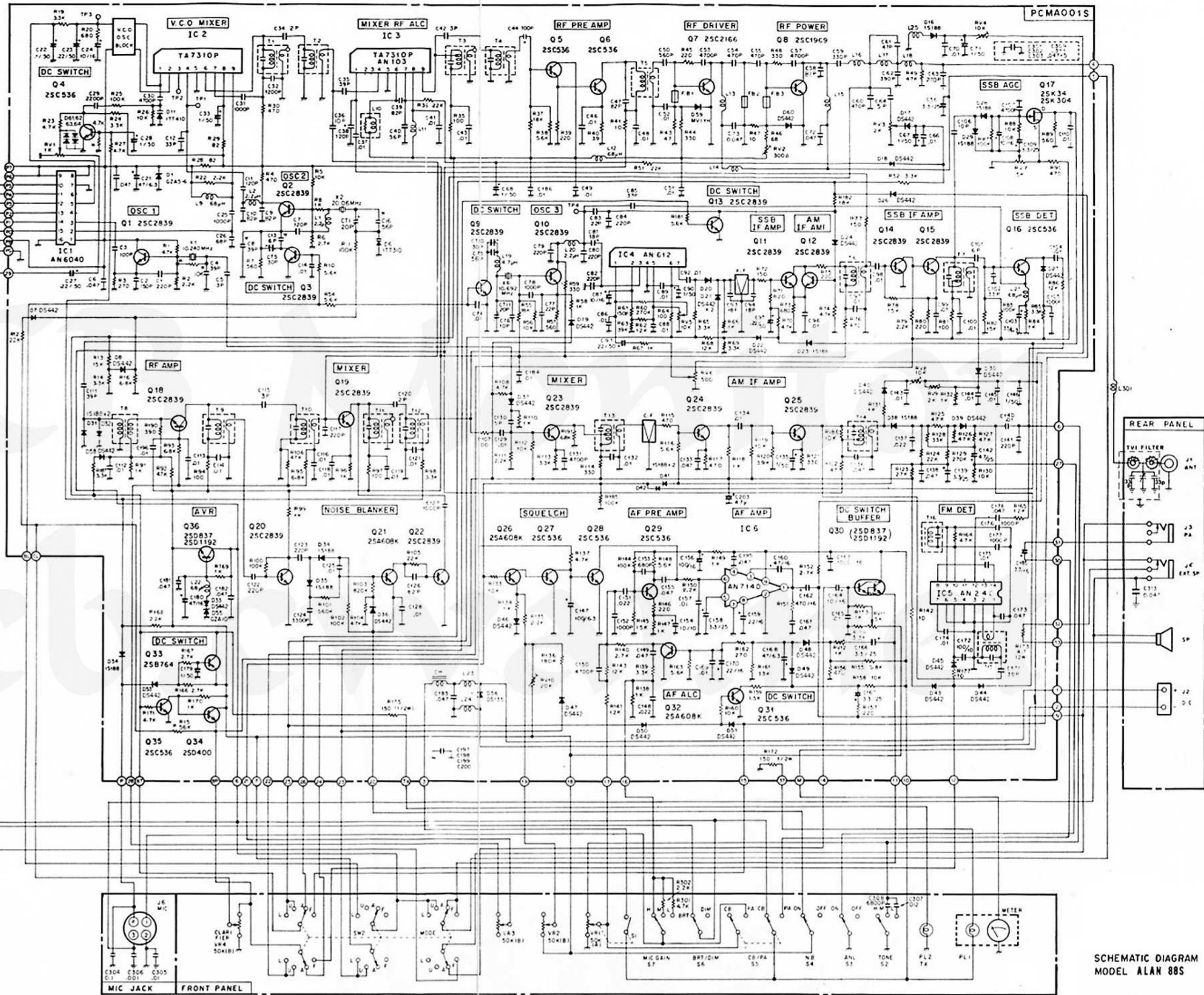
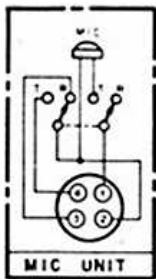
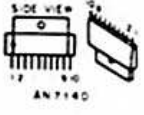
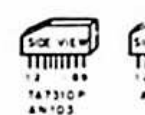
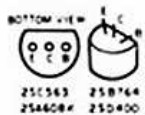
NOTE:
ALL VOLTAGES MEASURED FROM PC BOARD
GROUND WITH DC VTVM AT NO SIGNAL
(AT 13.8V POWER SUPPLY) IF MEASUREMENT
VALUES OBTAINED ARE IN EXCESS OF $\pm 20\%$
OF VALUES SHOWN THEN REASON FOR
DIFFERENCE SHOULD BE CORRECTED.

ΔTX : \odot LSB : () AM TX
↓ CHASSIS GND PC BOARD GND
* ADJUSTED (TYPICAL VALUE SHOWN)



TRANSISTOR BASE INFORMATION

E - EMITTER	S - SOURCE
C - COLLECTOR	G - GATE
B - BASE	D - DRAIN



SCHEMATIC DIAGRAM
MODEL ALAN 88S